

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S87	94	dma near3 (ready done finish\$3 complet\$3) near7 (status near3 (flag bit register))	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 12:56
S89	70	S87 and (memory sdram) adj2 (controller interface)	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 12:57
S88	1	S87 same (memory sdram) adj2 (controller interface)	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 12:59
S90	1	"20070083682".pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:26
S93	0	US2003149808.pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:32
S92	0	"2003149808".pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:32
S91	1	"5761532".pn.	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:32
S95	2	S94 and joshi	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:33
S94	165	(robert near2 burton).in.	US-PGPUB; USPAT; EPO	OR	OFF	2007/06/06 13:33
S96	8	("4482982" "4716523" "5333274" "5499384" "5659720" "5826107" "6081852" "6484217").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:00
S98	975	S97 same (set sets setting clear\$3) near3 (flag\$1 bit\$1)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:02
S100	212	S98 same status same (read\$3 writ\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:03
S99	210	S98 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:03
S102	37	(upper lower) adj (bound adj register)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:18
S105	3	S104 and dma	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:29

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S10 4	25	"upper bound register"	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:29
S10 3	4	S102 same dma	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:29
S97	8514	dma near2 transfer\$4	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:43
S10 6	4	(dma near5 busy) with (status near3 (flag\$1 bit\$1))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:46
S10 9	51	S107 S108	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:48
S10 8	25	(dma near3 (while during)) with (flag bit) near3 set	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:48
S10 7	26	dma with (busy near3 (flag bit))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:48
S10 1	53	S100 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:48
S11 0	17	S109 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:51
S11 3	26	dma with (busy near3 (bit flag))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 14:55
S11 4	84	dma near3 (busy process progress) near5 (flag bit)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 17:34
S11 2	46	S111 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 17:34
S11 1	159	dma near3 (busy process progress) with (flag bit)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 17:34
S11 5	25	S114 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/06 17:48
S11 6	33	(dma near3 (transfer\$4 transaction)) same (busy near3 (status bit))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:01

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L3	149	2 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:01
L2	557	(status adj register) with dma	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:01
L4	135	2 same (dma near3 (done finish\$3 complet\$3 pend\$3 busy process progress))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:02
L7	95	6 with (memory near3 (read\$3 writ\$3 transfer\$4 transaction))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:05
L6	1862	busy near3 bit	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:05
L5	48	4 and 710/2\$1.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:05
L10	17	6 same "memory controller"	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:06
L9	0	7 same (memory adj controller)	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:06
L8	74	6 near6 (memory near3 (read\$3 writ\$3 transfer\$4 transaction))	US-PGPUB; USPAT; USOCR	OR	OFF	2007/06/07 12:06